

WEST Search History

DATE: Monday, April 24, 2006

| Side? | <u>Set</u> <u>Name</u> | <u>Query</u> | <u>Hit</u> <u>Count</u> |
|--------------------------|---------------------------|---|----------------------------|
| | | <i>DB=EPAB,JPAB; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L15 | (multi-chip or multichip) adj2 (package or module) and (multi-layer or multilayer) adj2 (wiring or substrate) | 57 |
| | | <i>DB=USPT; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L14 | L13 and (multi-layer or multilayer) adj2 (wiring or substrate) | 89 |
| <input type="checkbox"/> | L13 | L7 and (multi-chip or multichip) adj2 (package or module) | 355 |
| <input type="checkbox"/> | L12 | L7 and (multi-chip or multichip) | 396 |
| | | <i>DB=EPAB,JPAB; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L11 | L10 and (second or bottom or opposite or another) adj4 (surface or side) adj4 substrate | 2 |
| <input type="checkbox"/> | L10 | substrate and (connect\$ or coupl\$ or provid\$) near5 exclusive\$ | 147 |
| | | <i>DB=USPT; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L9 | L7 and (connect\$ or coupl\$ or provid\$) near5 exclusive\$ | 20 |
| <input type="checkbox"/> | L8 | L7 and exclusive\$ | 279 |
| <input type="checkbox"/> | L7 | L6 and chip\$ | 3312 |
| <input type="checkbox"/> | L6 | L5 and data | 4129 |
| <input type="checkbox"/> | L5 | L4 and (chip\$ or IC\$) | 13449 |
| <input type="checkbox"/> | L4 | L2 and (second or bottom or opposite or another) adj4 (surface or side) adj4 substrate | 32815 |
| <input type="checkbox"/> | L3 | L2 and (second or bottom or opposite) adj4 (surface or side) adj4 substrate | 0 |
| <input type="checkbox"/> | L2 | substrate | 558570 |
| | | <i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L1 | substrate | 843280 |

END OF SEARCH HISTORY